

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <div style="text-align: center; border: 1px solid black; border-radius: 50%; width: 60px; margin: 0 auto;"> DEC 21 2005 W F </div>		Attorney Docket Number	3382-66931-01
		Application Number	10/765,717
		Filing Date	January 26, 2004
		First Named Inventor	Qadeer
		Art Unit	2185
		Examiner Name	Not yet assigned
Examiner's Initials (Handwritten)	RE	OTHER DOCUMENTS	
AD		Ball et al., "The SLAM project: debugging system software via static analysis." <i>Proceedings of the 29th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL '02)</i> , ACM Press, 2002, 3 pages.	
AD		Bouajjani et al., "A generic approach to the static analysis of concurrent programs with procedures." <i>Proceedings of the 30th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL '03)</i> , ACM Press, 2003, 12 pages.	
AD		Bush et al., "A static analyzer for finding dynamic programming errors." <i>Software-Practice and Experience</i> , 30(7):775-802, June 2000.	
AD		Corbett et al., "Bandera: extracting finite-state models from Java source code." <i>International Conference on Software Engineering</i> , 2000, pages 439-448.	
AD		Das et al., "ESP: Path-sensitive program verification in polynomial time." <i>Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation (PLDI '02)</i> , ACM Press, 2002, 12 pages.	
AD		Das, "Unification-based pointer analysis with directional assignments." <i>Proceedings of the ACM SIGPLAN 2000 Conference on Programming Language Design and Implementation (PLDI '00)</i> , ACM Press, 2000, pages 35-46.	
AD		Engler et al., "Checking system rules using system-specific, programmer-written compiler extensions." <i>Proceedings of the 4th Symposium on Operating Systems Design and Implementation (OSDI '00)</i> , Usenix Association, 2000, 16 pages.	
AD		Flanagan et al., "Thread-modular model checking." <i>Proceedings of the SPIN Workshop on Software Verification</i> , 2003, 15 pages.	
AD		Havelund et al., Model checking Java programs using Java PathFinder." <i>Software Tools for Technology Transfer (STTT)</i> , 2(4):72-84, 2000.	
AD		Henzinger et al., "Thread-modular abstraction refinement." <i>CAV 2003: Computer Aided Verification</i> , 2003, 13 pages.	
AD		Henzinger et al., "Lazy abstraction." <i>Proceedings of the 29th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL '02)</i> , ACM Press, 2002, 13 pages.	
AD		Holzmann, "The Model Checker SPIN." <i>IEEE Transactions on Software Engineering</i> , 23(5):279-295, May 1997.	
AD		Reps et al., "Precise interprocedural dataflow analysis via graph reachability." <i>Proceedings of the 22nd ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages</i> , ACM, 1995, pages 49-61.	
EXAMINER SIGNATURE:		/Anna Deng/	DATE CONSIDERED: 01/18/2007
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Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS	
AD		Robby et al., "Bogor: an extensible and highly-modular software model checking framework." <i>FSE 2003: Foundations of Software Engineering</i> , 2003, 10 pages.	
AD		Sharir et al., "Two approaches to interprocedural data flow analysis." <i>Program Flow Analysis: Theory and Applications</i> , Prentice-Hall, 1981, pages 189-233.	

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